

Fig. 1

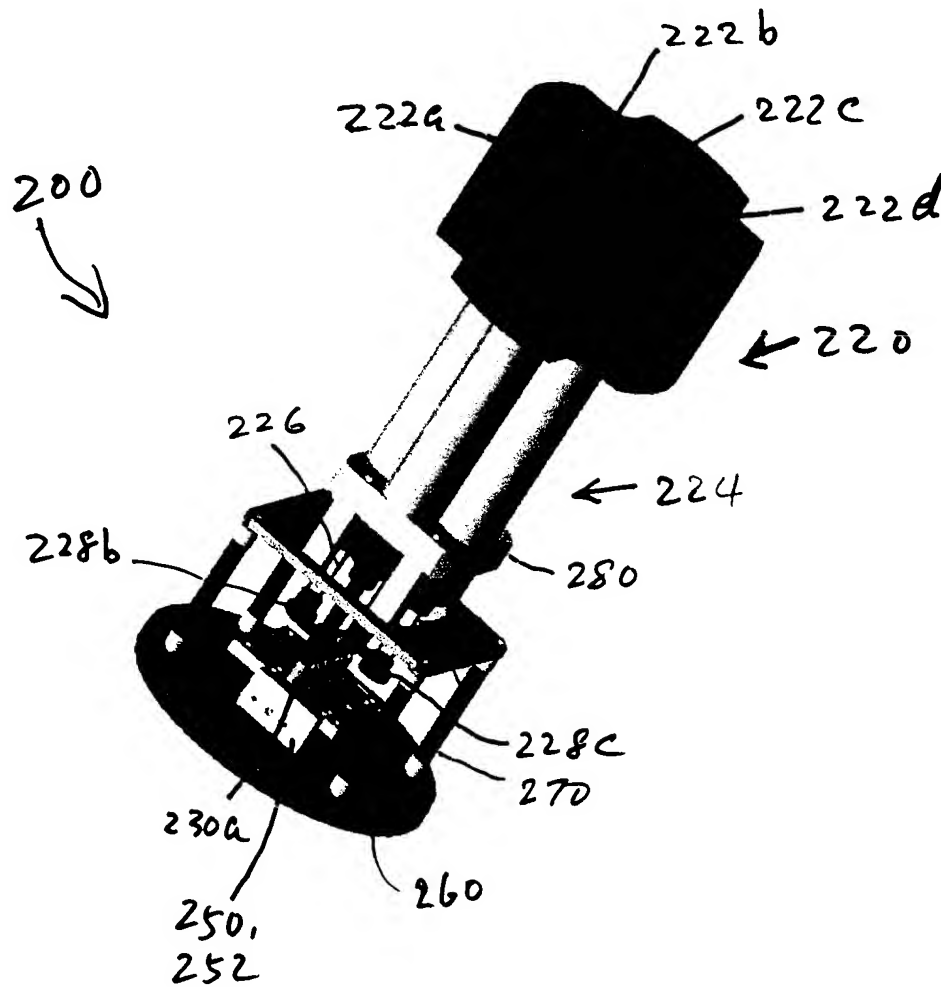


Fig. 2

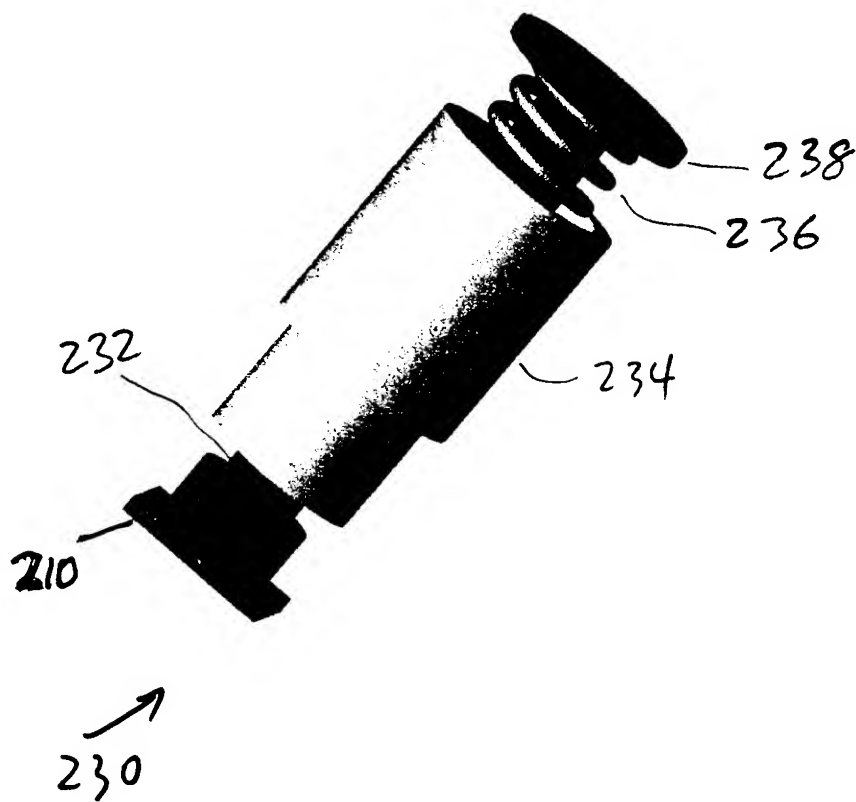


Fig. 3

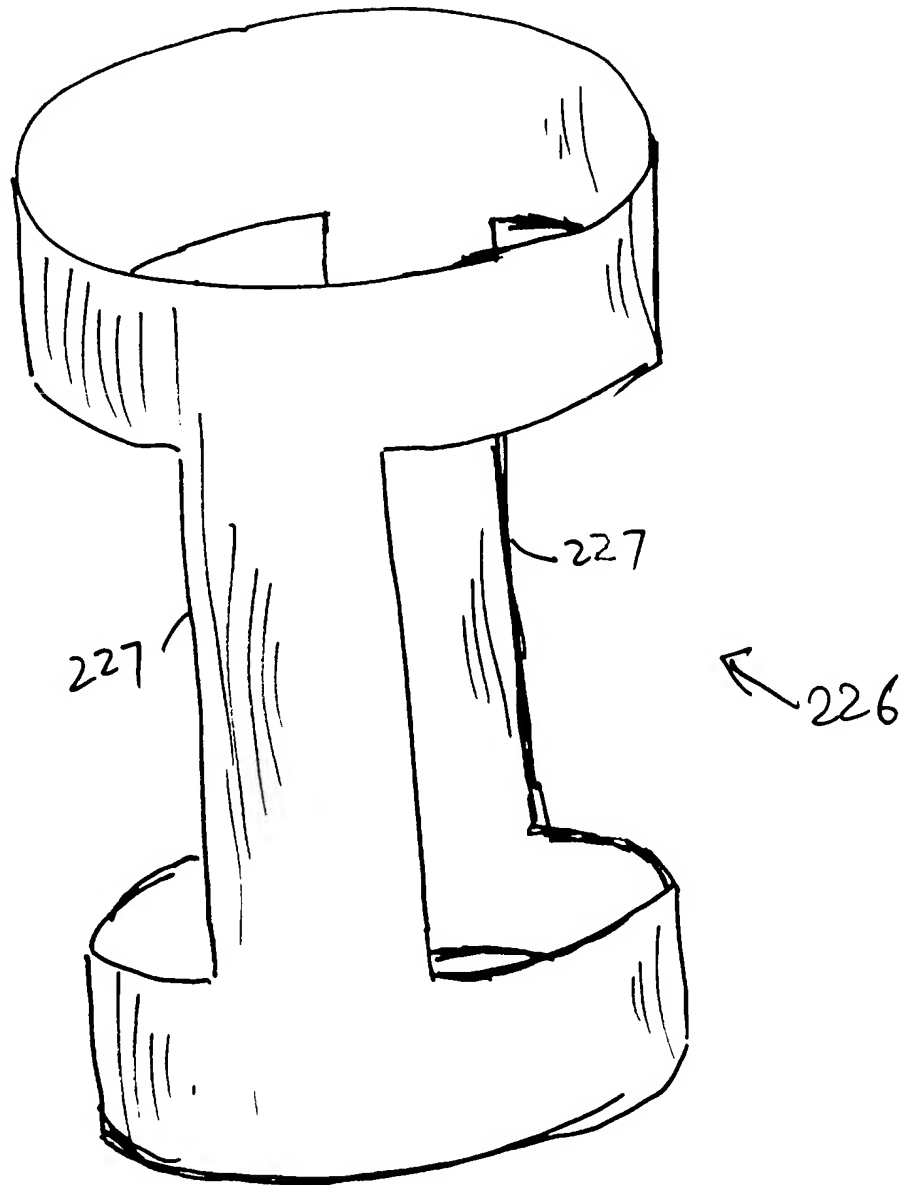


Fig. 4

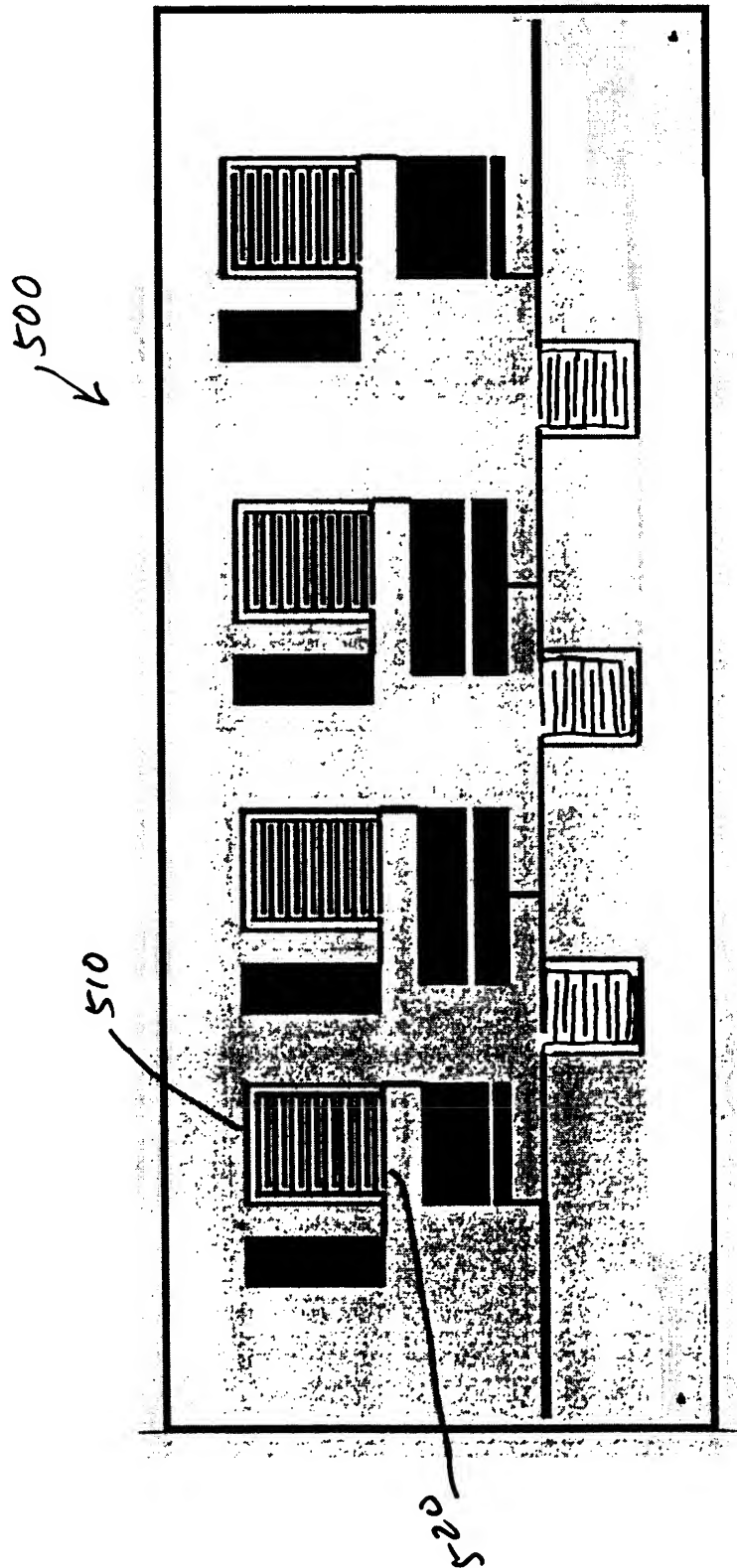


Fig. 5

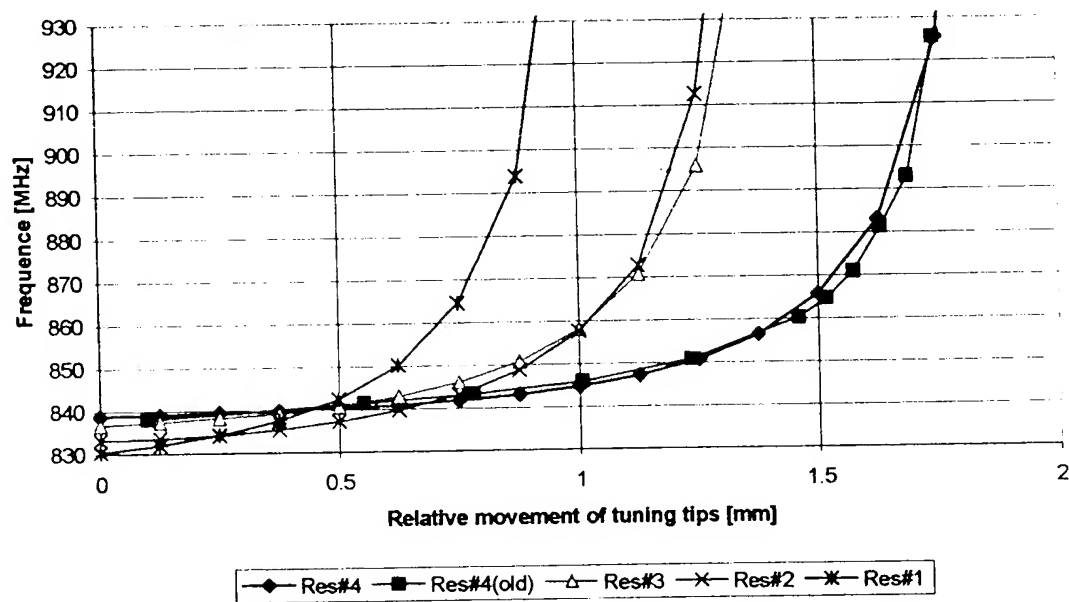


Fig. 6

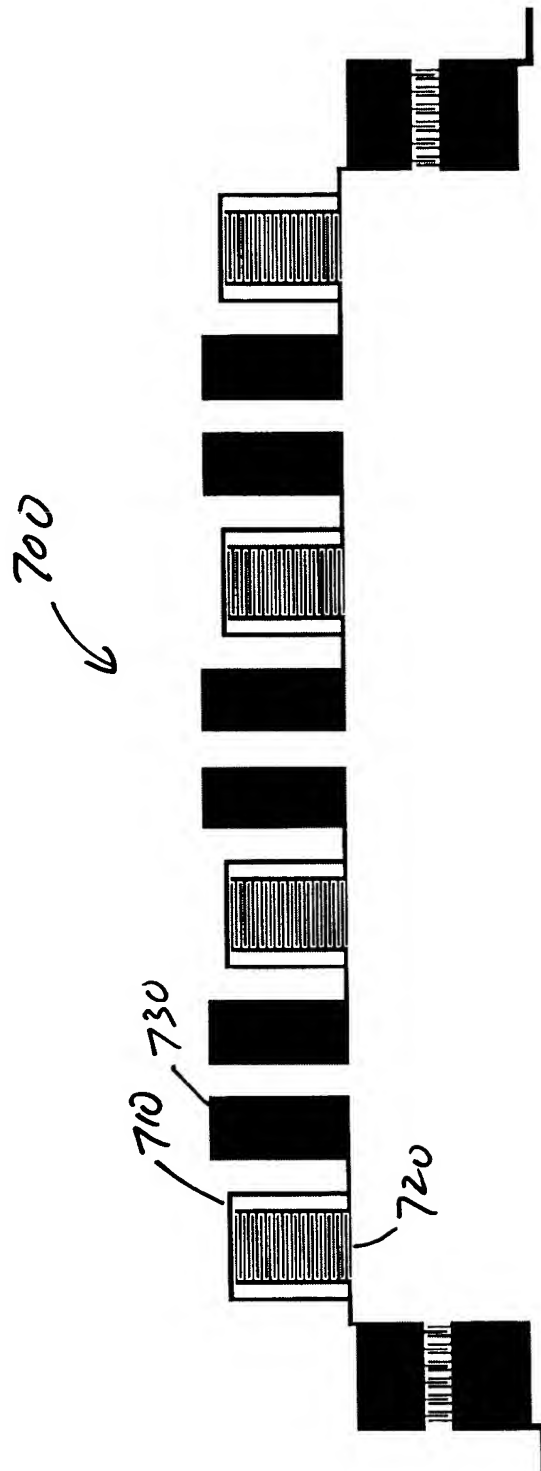


Fig. 7

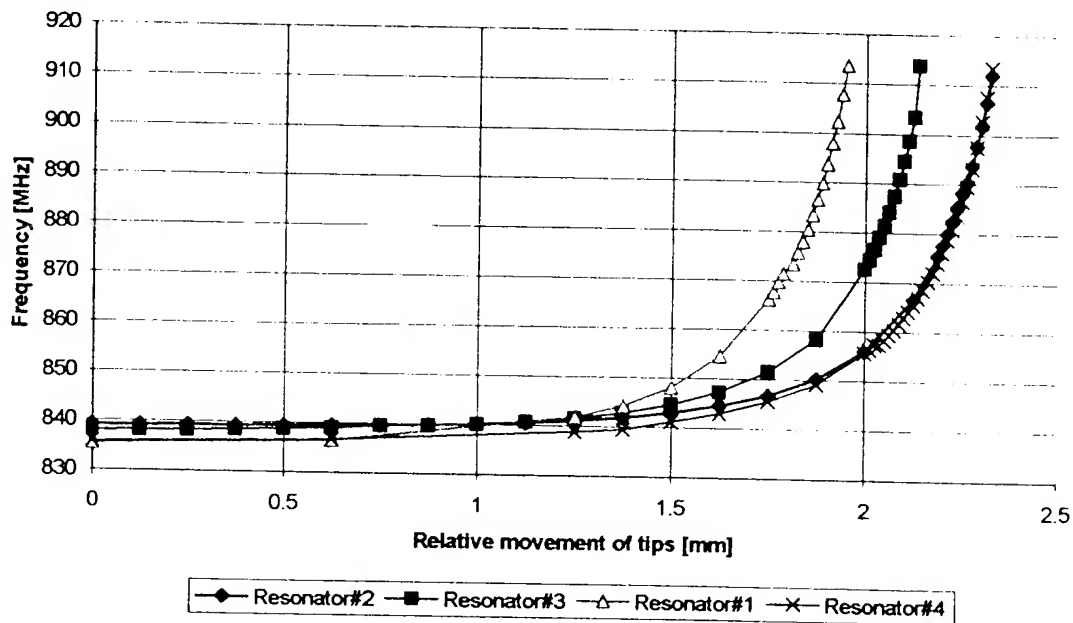


Fig. 8

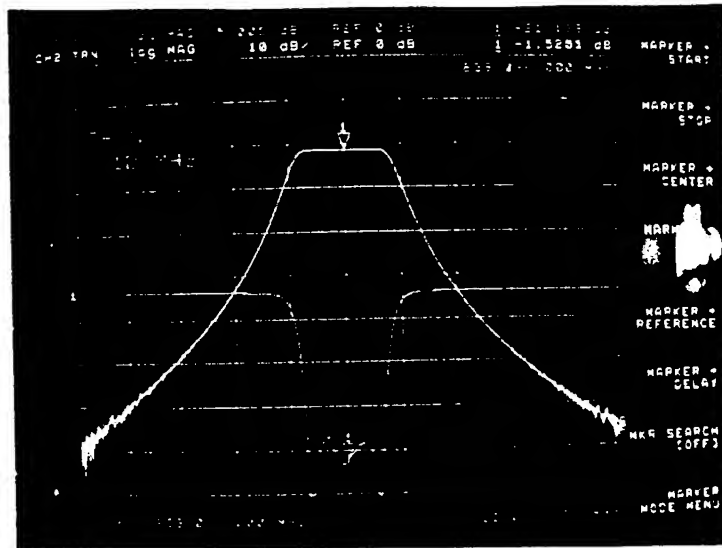


Fig. 9(a)

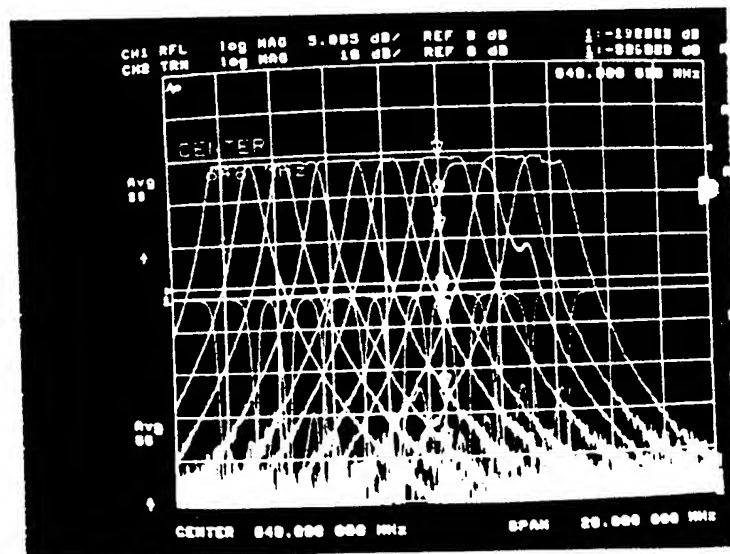


Fig. 9(b)

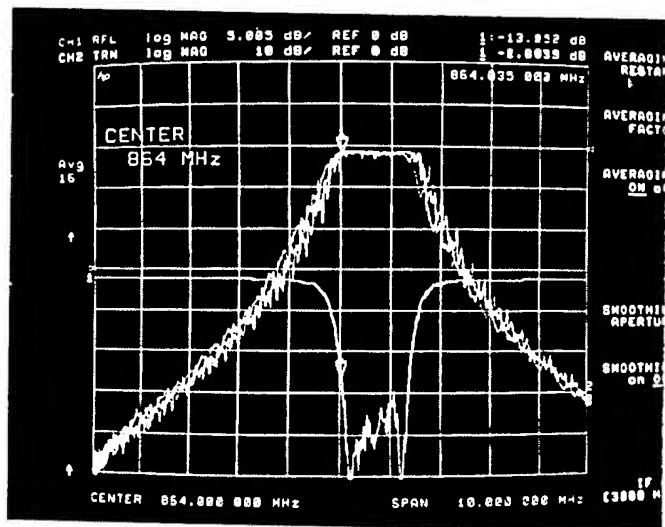


Fig 10(a)

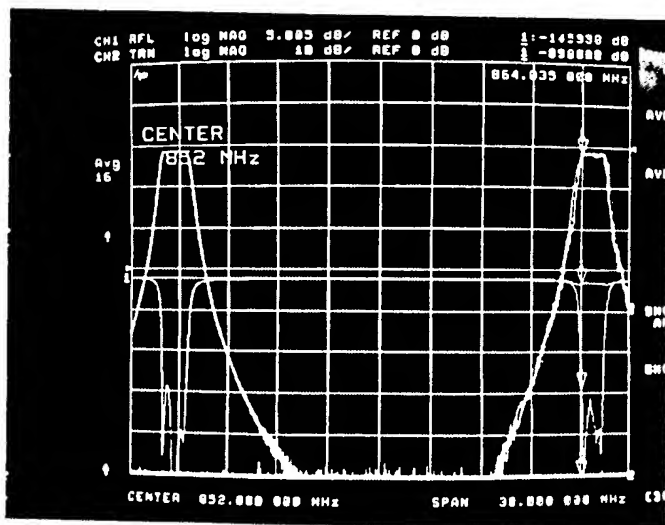
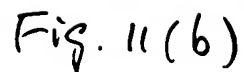
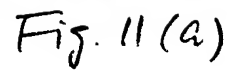


Fig. 10 (b)



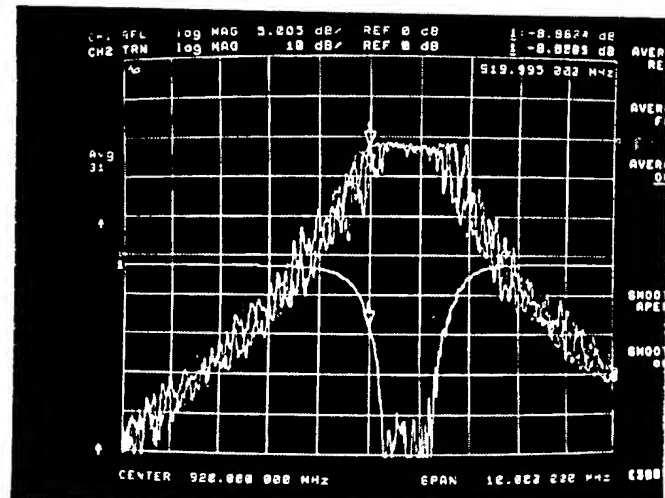


Fig. 12(c)

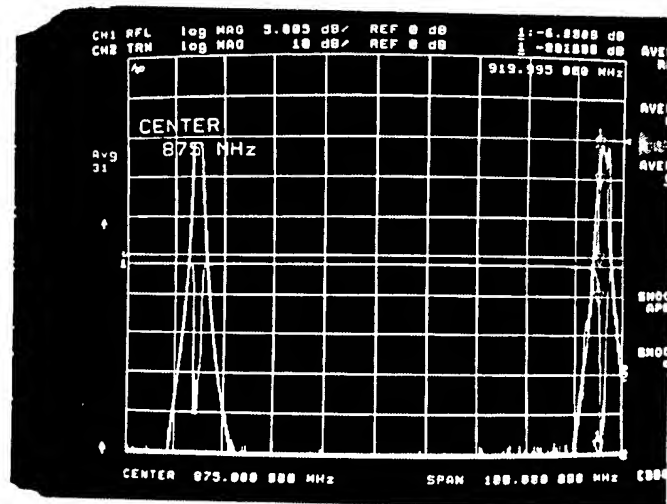


Fig. 12(b)

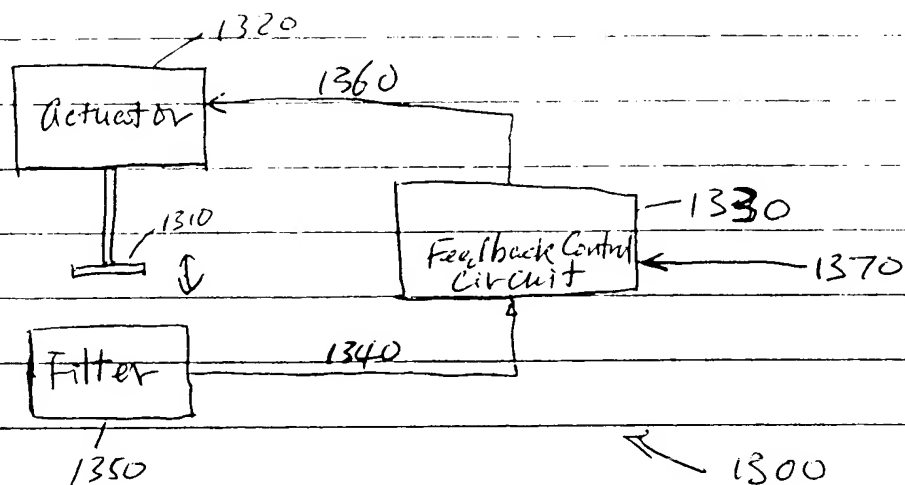


Fig. 13

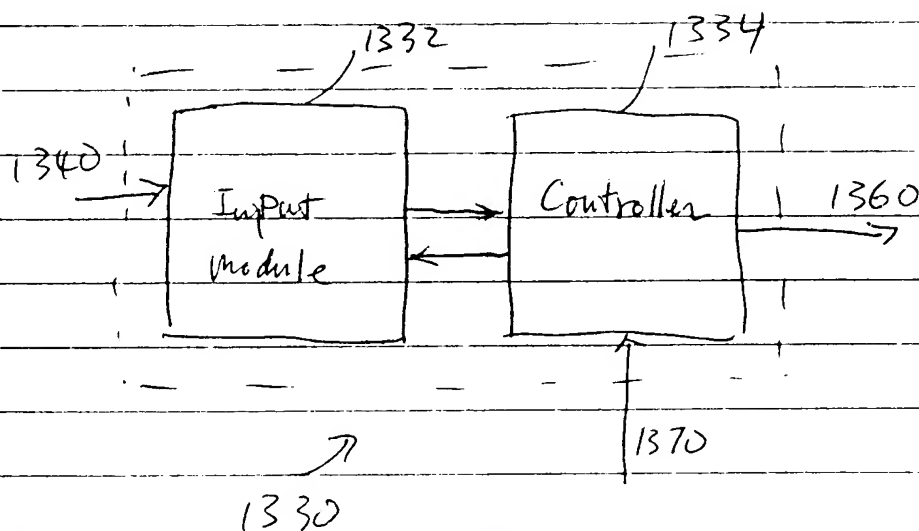
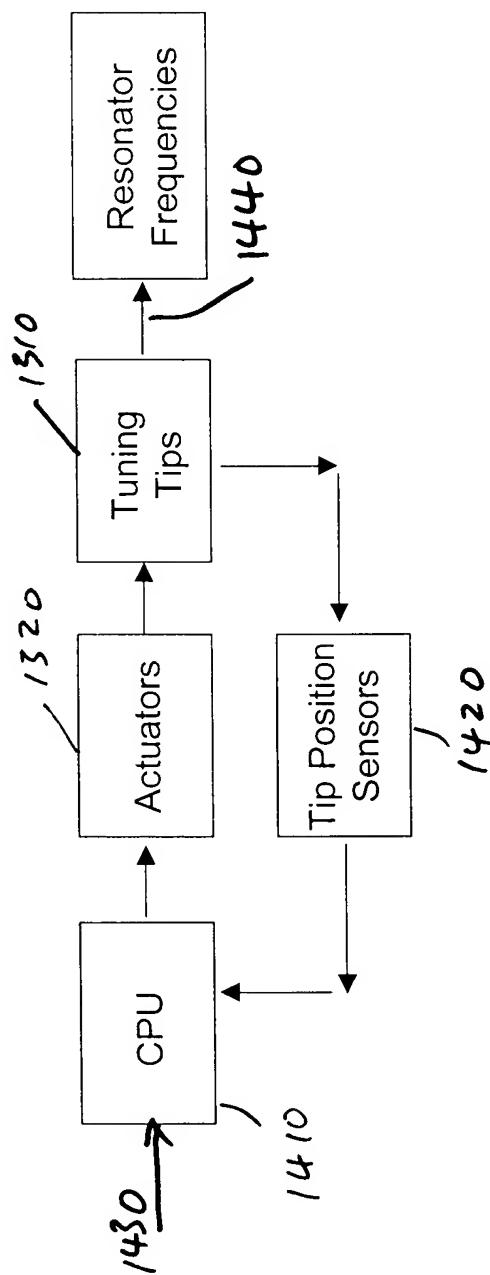


Fig. 14(a)

Figure 14b



Control System Block Diagram

1400

Figure 15a

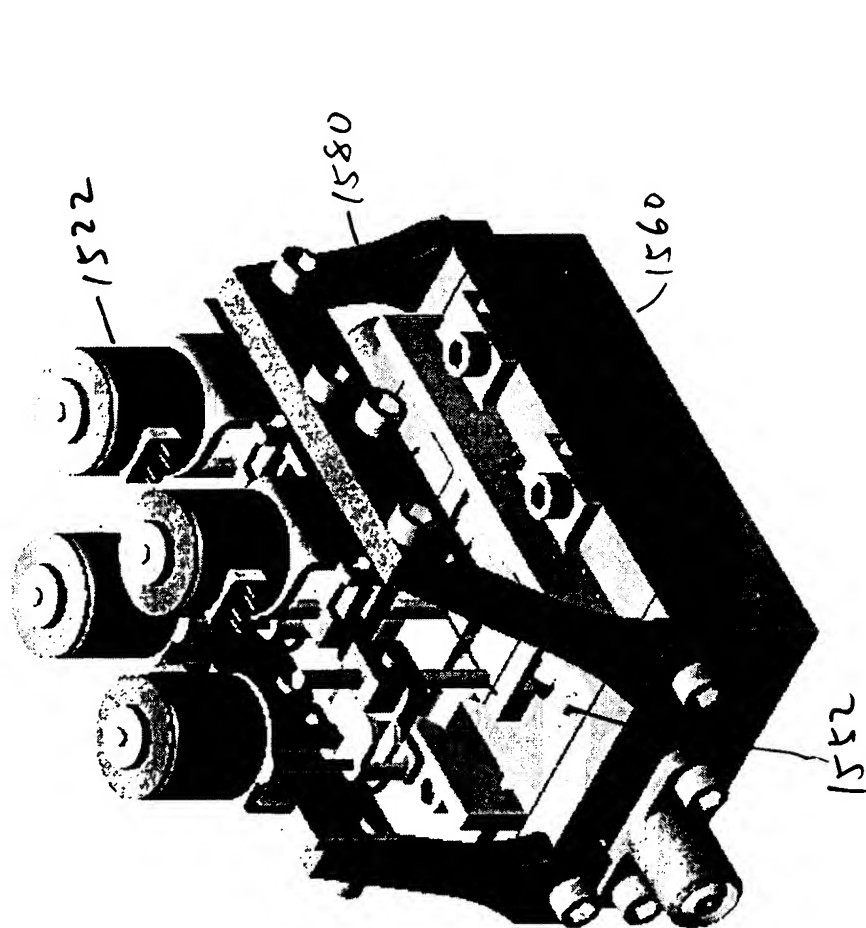
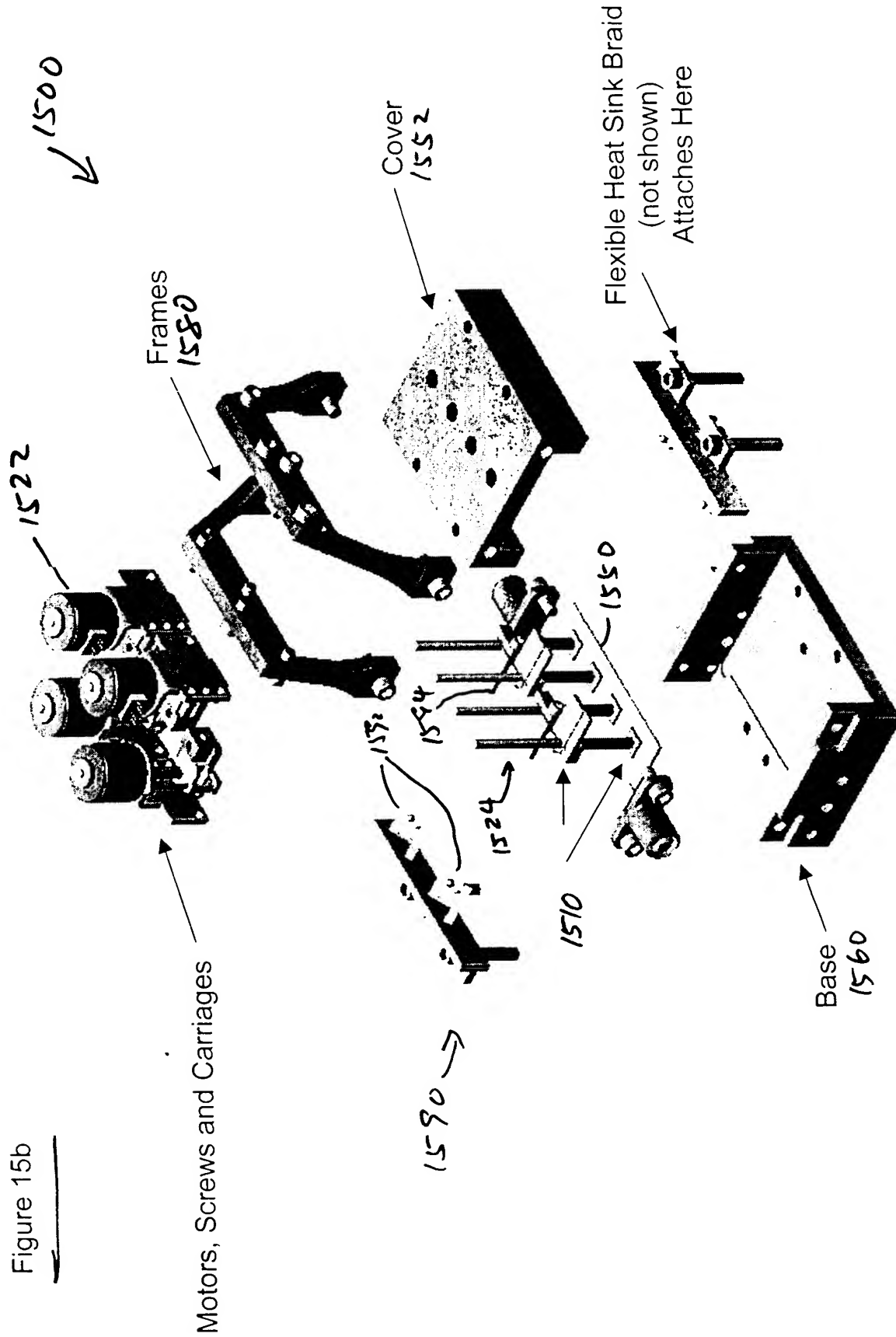


Figure 15b



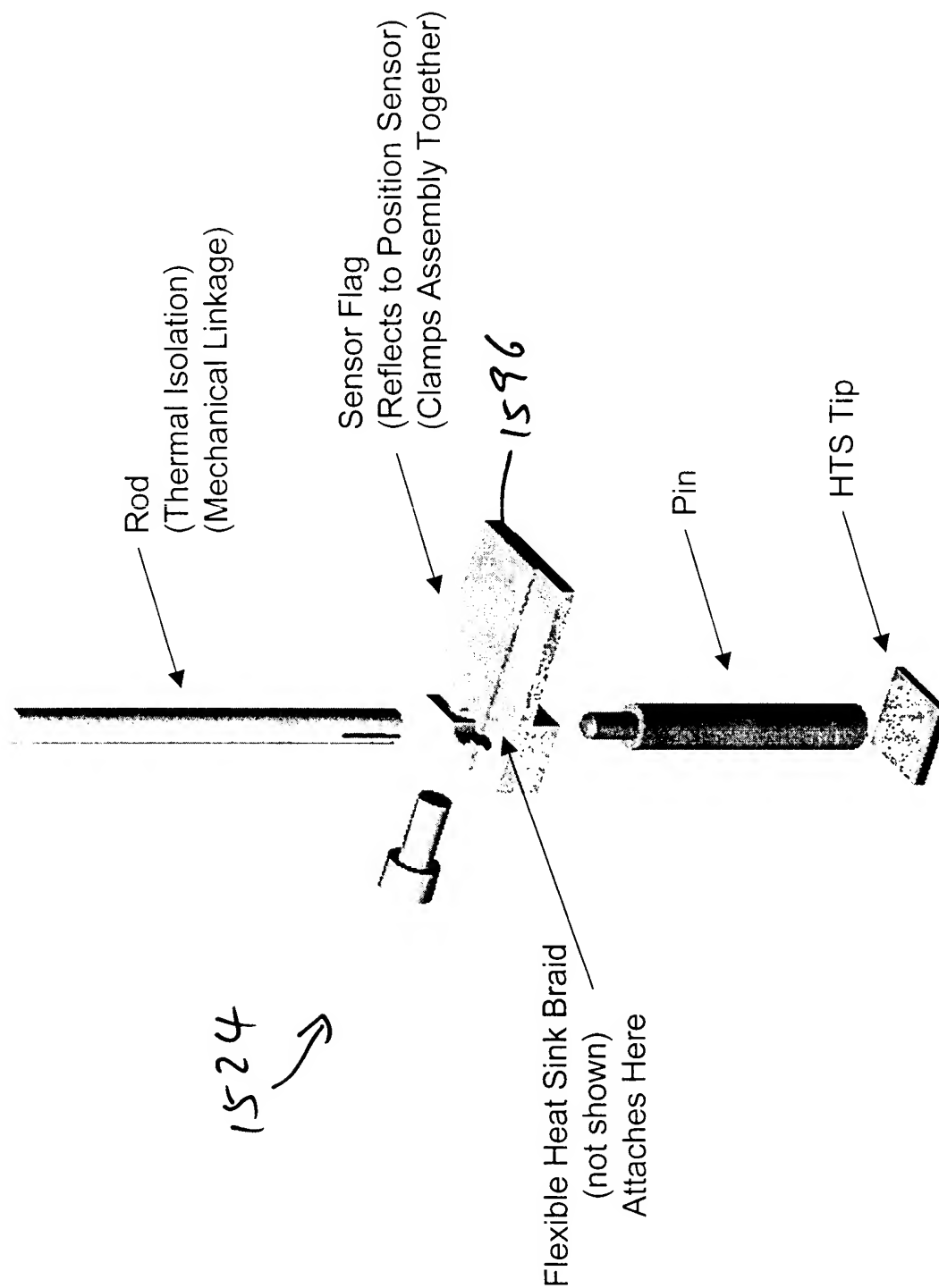


Figure 15c

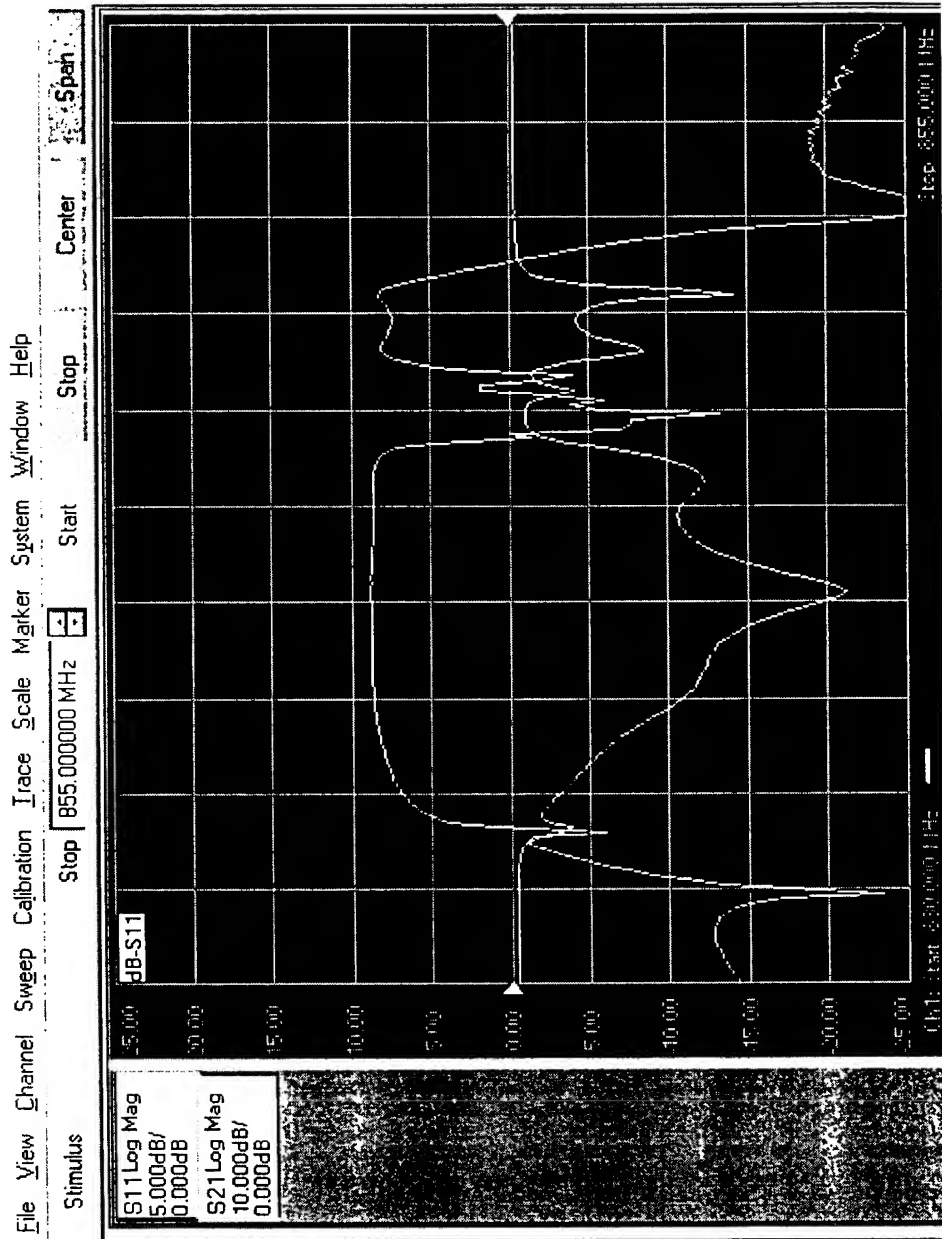


Fig. 16(a)

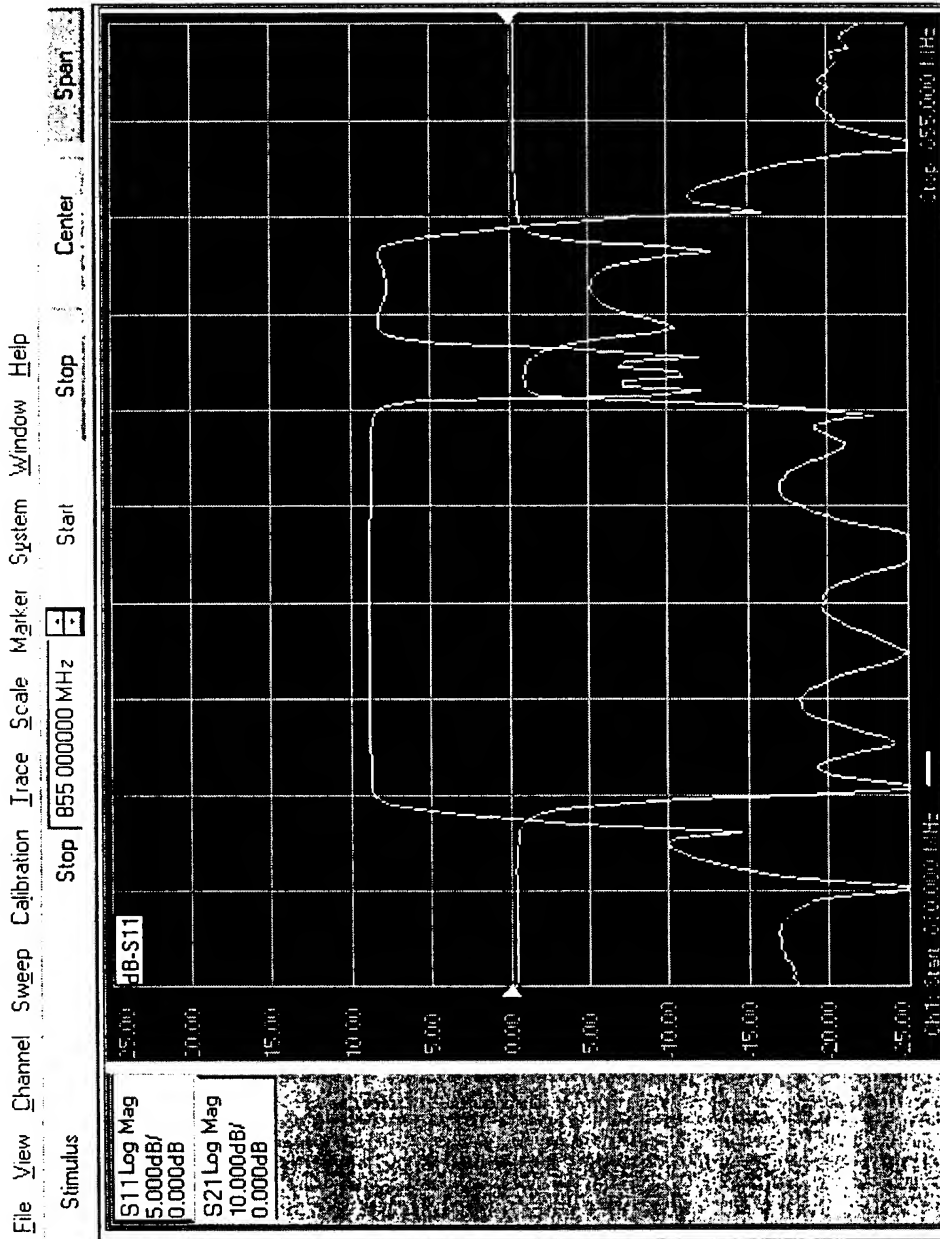


Fig. 16(b)

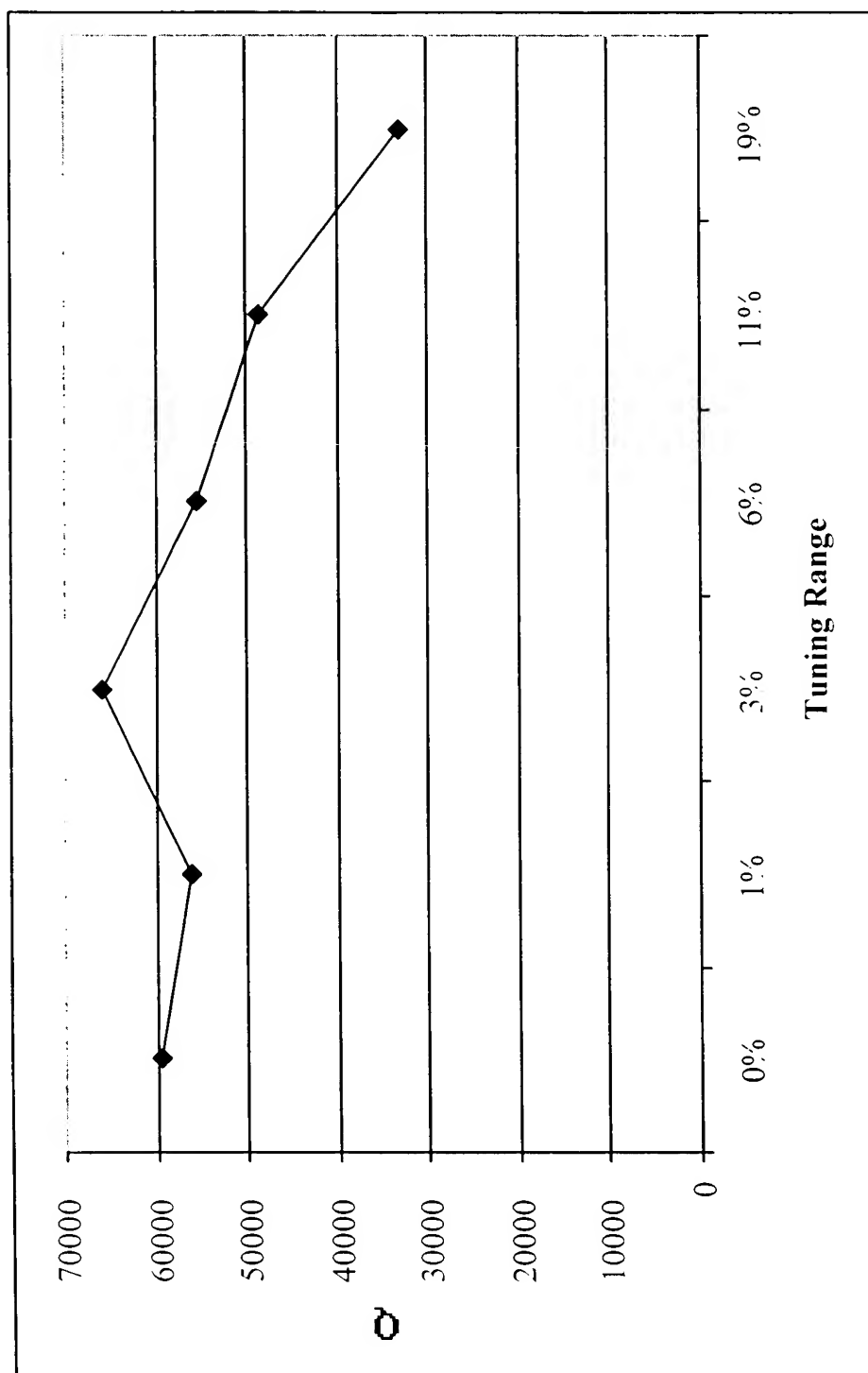


Fig. 17

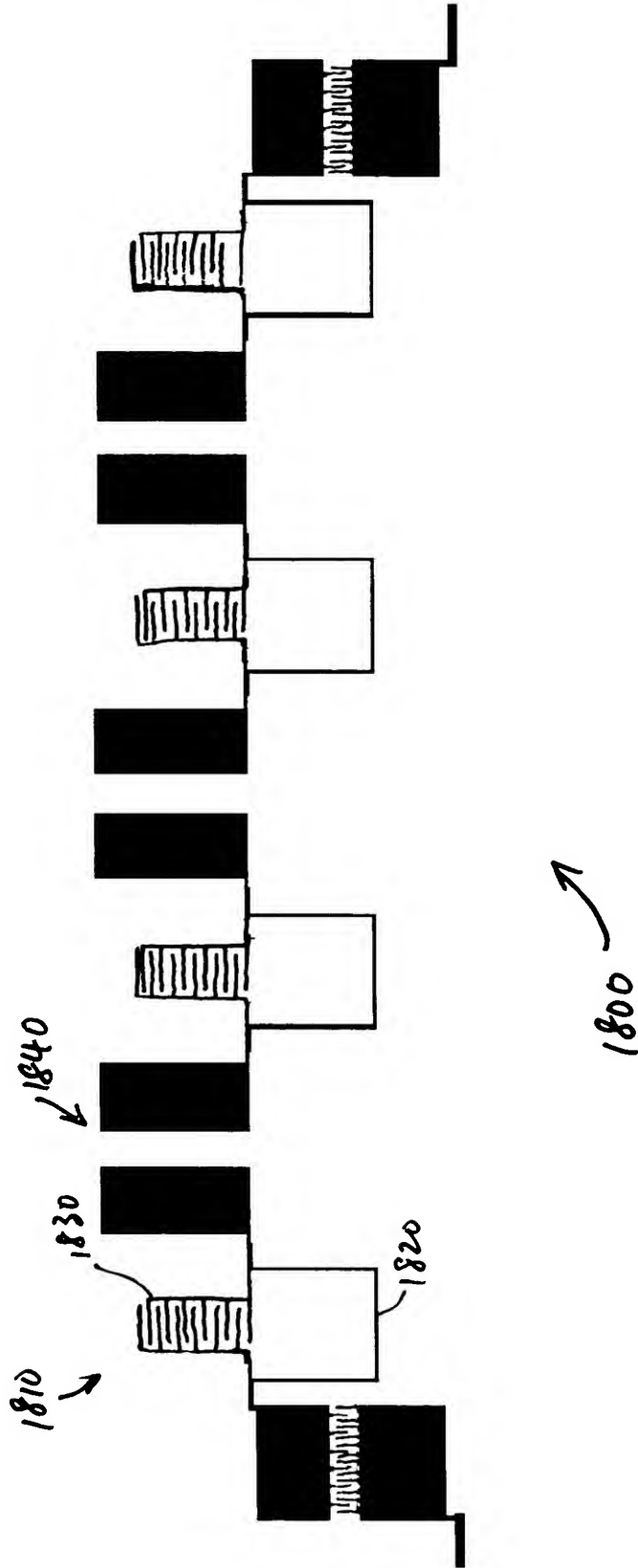


Fig. 18(a)

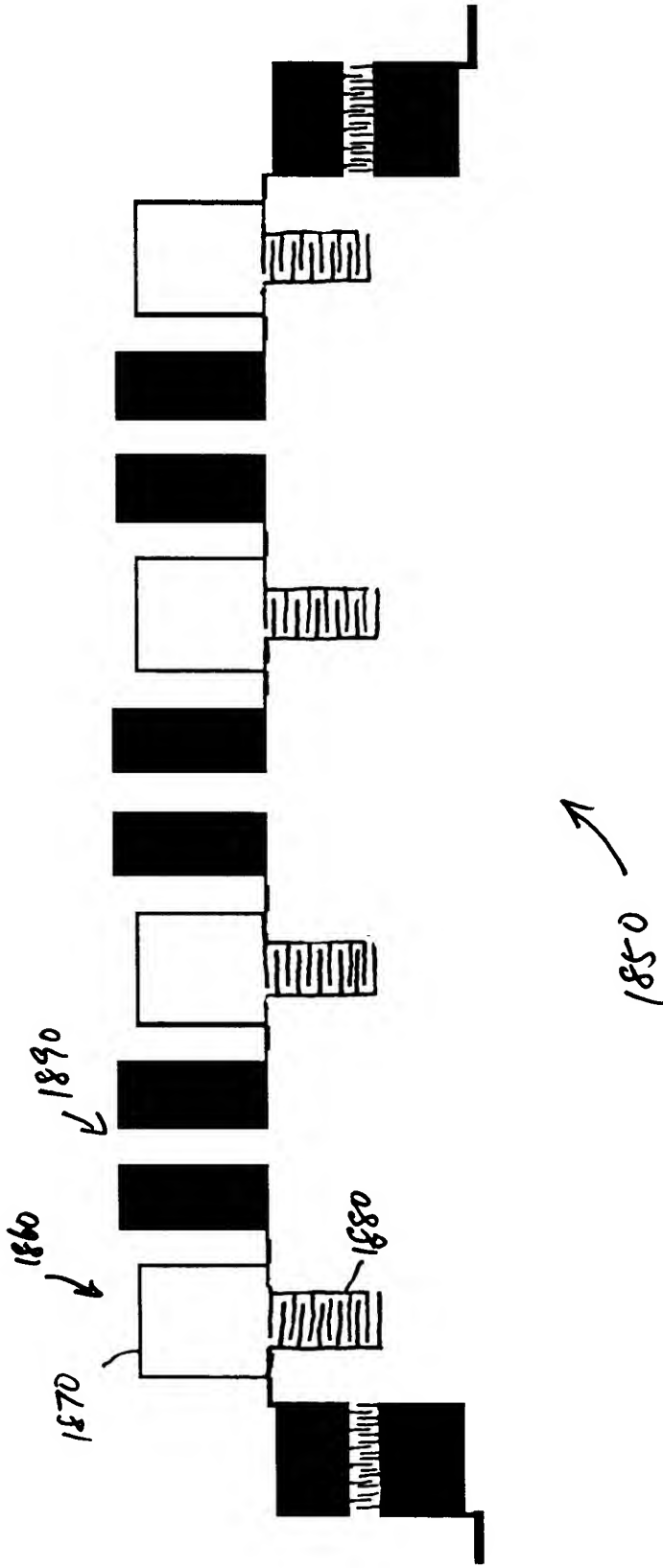


Fig. 18(b)

HTS Tip Tuning of Inductor vs Capacitor

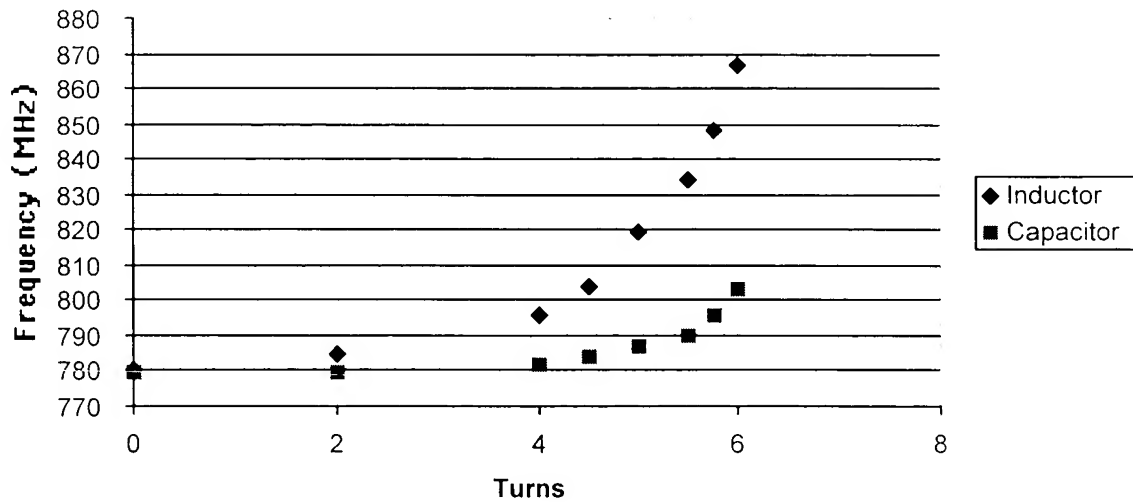


Fig. 19

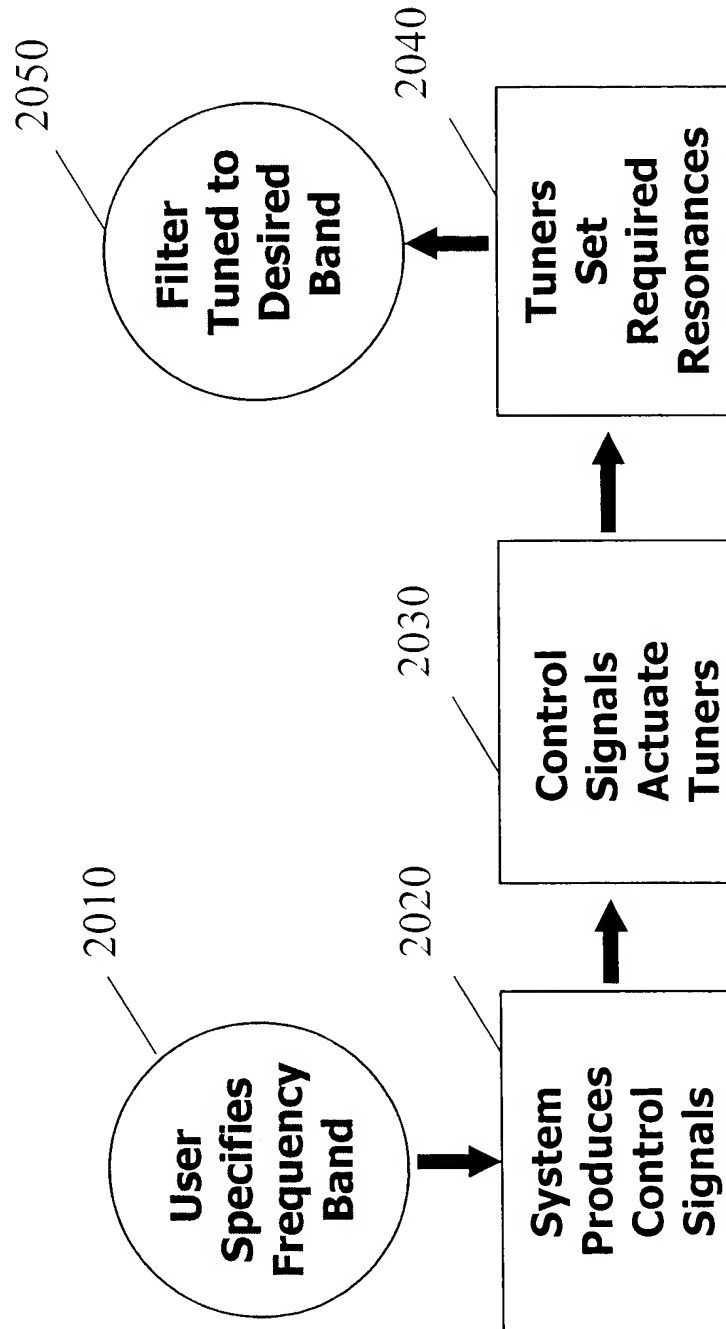


Fig. 20